## **PATENT APPLICATION**

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Tsuyoshi YONEYAMA

Application No.: New U.S. Patent Application

Filed: February 5, 2002 Docket No.: 111907

DISPLAY DRIVING CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT, DISPLAY

PANEL AND DISPLAY DRIVING METHOD

## **INFORMATION DISCLOSURE STATEMENT**

Director of the U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

For:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- 2. Relevance of the non-English language references are discussed in the present specification.
- 3. English-language Abstracts of the non-English language references are attached hereto.

Respectfully submitted,

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Date: February 5, 2002

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